

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"20040003352"	US-PGPUB; USPAT	OR	OFF	2004/08/16 09:47
S2	212	"process management system"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:38
S3	35	"process management system" & JAVA	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:38
S4	375	"process management" & JAVA	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:07
S5	176	("process management" & JAVA) & HTML	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:07
S6	32	("process management" & JAVA) & "shopping cart"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:07
S7	94	"data management system" & JAVA & custom	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:44
S8	0	"data management system" & JAVA & "custom data field"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:35
S9	0	"data management system" & JAVA & "custom field\$"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:35
S10	7	"data management system" & JAVA & "custom data\$"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:35
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S12	1	"process management system" & JAVA & "custom field\$"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:38
S13	2	"process management system" & "custom data\$"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:39
S14	9	"custom\$ data field\$"	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:39
S15	75	"data management system" & JAVA & custom & model	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:45
S16	67	"data management system" & JAVA & custom & model & file	US-PGPUB; USPAT	OR	OFF	2004/08/16 10:45
S17	1	"5920867".pn.	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:06
S18	1	"5920867".pn. & (create same file)	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:08
S19	0	"5920867".pn. & (create same file same custom)	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:08
S20	0	"5920867".pn. & ("create file")	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:09
S21	4	"process management system" & "create file"	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:12
S22	209	iplanet	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:14

S23	8	iplanet & "process management"	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:12
S24	1	iplanet & "create file"	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:15
S25	1	"5201047".pn.	US-PGPUB; USPAT	OR	OFF	2004/08/16 11:59
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S27	197	S26 & "process management"	US-PGPUB; USPAT	OR	OFF	2005/03/17 17:13
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S42	12	"custom\$ data field\$"	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:30
S43	102	"data management system" & JAVA & custom & model	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:30
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S56	160	S55 & custom	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:30
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S61	3176	"process management"	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:44
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S64	73	S63 & compress\$	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:56
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S66	10	S65 & database	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:58
S67	1	S65 & alistair	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:58
S68	0	papiani.in.	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:58
S69	268	hey.in.	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:58
S70	8	S69 & database	US-PGPUB; USPAT	OR	OFF	2005/12/06 08:59


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Relevance scale

1 A distributed scientific data archive using the Web, XML and SQL/MED


Mark Papiani, Jasmin L. Wason, Alistair N. Dunlop, Denis A. Nicole

 September 1999 **ACM SIGMOD Record**, Volume 28 Issue 3

Publisher: ACM Press

 Full text available: [pdf\(794.86 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

We have developed a web-based architecture and user interface for fast storage, searching and retrieval of large, distributed, files resulting from scientific simulations. We demonstrate that the new DATALINK type defined in the draft SQL Management of External Data Standard can help to overcome problems associated with limited bandwidth when trying to archive large files using the web. We also show that separating the user interface specification from the user interface processing can prov ...

2 Generation of software tools from processor descriptions for hardware/software codesign


Mark R. Hartoog, James A. Rowson, Prakash D. Reddy, Soumya Desai, Douglas D. Dunlop, Edwin A. Harcourt, Neeti Khullar

 June 1997 **Proceedings of the 34th annual conference on Design automation - Volume 00**
Publisher: ACM Press

 Full text available: [pdf\(56.71 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
[Publisher Site](#)

An experimental set of tools that generate instruction set simulators, assemblers, and disassemblers from a single description was developed to test if retargetable development tools would work for commercial DSP processors and microprocessors. The processor instruction set was described using a language called nML. The TMS320C50 DSP processor and the ARM7 microprocessor were modeled in nML. The resulting instruction set models execute about 25,000 instructions per second, and compiled instruction sets ...

3 Supercomputing with transputers—past, present and future


Anthony J. G. Hey

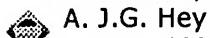
 June 1990 **ACM SIGARCH Computer Architecture News, Proceedings of the 4th international conference on Supercomputing ICS '90**, Volume 18 Issue 3b

Publisher: ACM Press

 Full text available: [pdf\(1.24 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

terms

The paper traces the development of large multi-transputer systems for high-performance scientific and engineering computing. After defining what we mean by 'supercomputing' in the context of this paper, the past and present state of transputer supercomputing environments is illustrated by a discussion of three specific projects. These are the Esprit 'SuperNode' project or P1085, the Edinburgh Concurrent Supercomputer project and the Victor project at IBM Research in Yorktown Heights. The p ...

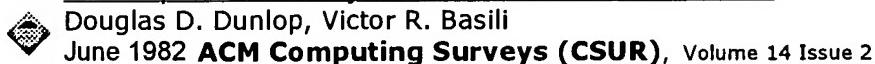
4 Practical parallel processing with transputers 

A. J.G. Hey
January 1988 **Proceedings of the third conference on Hypercube concurrent computers and applications: Architecture, software, computer systems, and general issues - Volume 1**

Publisher: ACM Press

Full text available: pdf(460.56 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper explores methods for extracting parallelism from a wide variety of numerical applications. Load-balancing considerations and communication overheads for implementing scientific and engineering problems on networks of transputers are then discussed. Some case studies are presented in detail. The paper demonstrates that reconfigurable transputer arrays provide a flexible, powerful and easily programmable approach to parallel processing.

5 A Comparative Analysis of Functional Correctness 

Douglas D. Dunlop, Victor R. Basili
June 1982 **ACM Computing Surveys (CSUR)**, Volume 14 Issue 2

Publisher: ACM Press

Full text available: pdf(1.28 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

6 Will cell generation displace standard cells?

Alfred E. Dunlop

June 1988 **Proceedings of the 25th ACM/IEEE conference on Design automation**

Publisher: IEEE Computer Society Press

Additional Information: [full citation](#), [abstract](#), [index terms](#)

Standard cells are now a mainstay of the ASIC industry. They are characterized by a well understood library of cells, each of which has a fixed geometry and, in particular, fixed transistor sizes. Their advantages include ease of use, predictability, and reasonably efficient use of silicon area. They normally require longer processing time than gate-array or sea-of-gates techniques because more fabrication steps are required, but in return provide higher levels of integration because of imp ...

7 Citation linking: improving access to online journals 

S. Hitchcock, L. Carr, S. Harris, J. M. N. Hey, W. Hall

July 1997 **Proceedings of the second ACM international conference on Digital libraries**

Publisher: ACM Press

Full text available: pdf(1.32 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: citation linking, electronic journals, hypermedia, hypertext, link services

8 Generalizing specifications for uniformly implemented loops

Douglas D. Dunlop, Victor R. Basili

January 1985 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,

Volume 7 Issue 1

Publisher: ACM Press

Full text available: pdf(1.54 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The problem of generalizing functional specifications for while loops is considered. This problem occurs frequently when trying to verify that an initialized loop satisfies some functional specification, i.e., produces outputs which are some function of the program inputs. The notion of a valid generalization of a loop specification is defined. A particularly simple valid generalization, a base generalization, is discussed. A property of many commonly occurring while loops, that ...

9 The effect of accessing nonmatching documents on relevance feedback

Mark D. Dunlop

April 1997 **ACM Transactions on Information Systems (TOIS)**, Volume 15 Issue 2

Publisher: ACM Press

Full text available: pdf(482.04 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Traditional information retrieval (IR) systems only allow users access to documents that match their current query, and therefore, users can only give relevance feedback on matching documents (or those with a matching strength greater than a set threshold). This article shows that, in systems that allow access to nonmatching documents (e.g., hybrid hypertext and information retrieval systems), the strength of the effect of giving relevance feedback varies between matching and nonmatching doc ...

Keywords: free-text information retrieval, hypertext, negative feedback, probabilistic model, relevance feedback, vector space model

10 Proceedings of the 30th international conference on Design automation

Alfred E. Dunlop

July 1993 proceeding

Publisher: ACM Press

Additional Information: [full citation](#), [index terms](#)

11 Image retrieval by hypertext links

V. Harmandas, M. Sanderson, M. D. Dunlop

July 1997 **ACM SIGIR Forum , Proceedings of the 20th annual international ACM SIGIR conference on Research and development in information retrieval SIGIR '97**, Volume 31 Issue SI

Publisher: ACM Press

Full text available: pdf(1.09 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

12 Time, relevance and interaction modelling for information retrieval

M. D. Dunlop

July 1997 **ACM SIGIR Forum , Proceedings of the 20th annual international ACM SIGIR conference on Research and development in information retrieval SIGIR '97**, Volume 31 Issue SI

Publisher: ACM Press

Full text available:  pdf(1.23 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



13 Editorial

John Dunlop, Luciano Lenzini, Enzo Mingozi

January 2004 **Wireless Networks**, Volume 10 Issue 1

Publisher: Kluwer Academic Publishers

Full text available:  pdf(32.49 KB) Additional Information: [full citation](#), [index terms](#)



14 SLIM-the translation of symbolic layouts into mask data

 A. E. Dunlop

June 1980 **Proceedings of the 17th conference on Design automation**

Publisher: ACM Press

Full text available:  pdf(622.10 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A new form of symbolic layout for integrated circuits is coupled with a mask compaction procedure which removes excess space while guaranteeing that all design rules are met. Tradeoffs between X and Y compaction are made based on critical path information. Two types of compaction are used to minimize mask area and computer run-time. Additional procedures reduce mask area by inserting jogs at strategic locations in the layout. A partitioned data base is used to store mask data in a hierarchi ...



15 Chip layout optimization using critical path weighting

A. E. Dunlop, V. D. Agrawal, D. N. Deutsch, M. F. Jukl, P. Kozak, M. Wiesel
June 1984 **Proceedings of the 21st conference on Design automation**

Publisher: IEEE Press

Full text available:  pdf(276.22 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A chip layout procedure for optimizing the performance of critical timing paths in a synchronous digital circuit is presented. The procedure uses the path analysis data produced by a static timing analysis program to generate weights for critical nets on clock and data paths. These weights are then used to bias automatic placement and routing in the layout program. This approach is shown to bring the performance of the chip significantly closer to that of an ideal layout which is assumed to ...



16 Chip layout optimization using critical path weighting

 A. E. Dunlop, V. D. Agrawal, D. N. Deutsch, M. F. Jukl, P. Kazak

June 1988 **Papers on Twenty-five years of electronic design automation**

Publisher: ACM Press

Full text available:  pdf(346.05 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



17 SLIM - The translation of symbolic layouts into mask data

 A. E. Dunlop

June 1988 **Papers on Twenty-five years of electronic design automation**

Publisher: ACM Press

Full text available:  pdf(776.48 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

18 The Challenge of Mobile Devices for Human Computer Interaction

Mark Dunlop, Stephen Brewster

January 2002 **Personal and Ubiquitous Computing**, Volume 6 Issue 4**Publisher:** Springer-VerlagFull text available: [pdf\(41.24 KB\)](#) Additional Information: [full citation](#), [index terms](#)**19 Global illumination: Importance sampling with hemispherical particle footprints**

Heinrich Hey, Werner Purgathofer

April 2002 **Proceedings of the 18th spring conference on Computer graphics****Publisher:** ACM PressFull text available: [pdf\(348.48 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We present a new importance sampling technique for stochastic ray-based global illumination methods. It allows to enhance the efficiency of global illumination calculations in general scenes with complex illumination settings by selecting preferably those sampling or shooting directions which yield a high contribution. The probability density functions for this are generated with a photon map or importance map that represents the expected contribution. An outgoing direction for a given point in ...

Keywords: global illumination, importance map, importance sampling, particle map, photon map

20 Novel user interfaces: A digital library of conversational expressions: helping

profoundly disabled users communicate

Hayley Dunlop, Sally Jo Cunningham, Matt Jones

July 2002 **Proceedings of the 2nd ACM/IEEE-CS joint conference on Digital libraries****Publisher:** ACM PressFull text available: [pdf\(434.19 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Digital libraries are for everyone. This paper describes the development of a digital library for a user who has a profound physical disability that means she cannot communicate verbally, and cannot use conventional communication tools.

Keywords: HCI, case study, communication aids, physical disabilities, user centered design

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